

JCS55 U.S. PTO  
10/028276  
12/28/01

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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10028276	12/28/2001	345	92	2673	Piziali

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\*\*CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 09/144,538 08/31/1998 PAT 6,335,716 - *ds*

\*\* FOREIGN APPLICATIONS VERIFIED:

JAPAN 9-254257 09/03/1997 *ds*

JAPAN 10-156696 05/20/1998 *ds*

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☒ yes ☐ no  
35 USC 119 conditions met ☒ yes ☐ no  
Verified and Acknowledged: J Examiners's initials *ds*

ATTORNEY DOCKET NO

740756-2414

TITLE : Semiconductor display device correcting system and correcting method of semiconductor display device

U.S. DEPT. OF COMM./PAT. & TM-PTO-435L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>Application Examiner</b>	
		<b>PREPARED FOR ISSUE</b>	
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